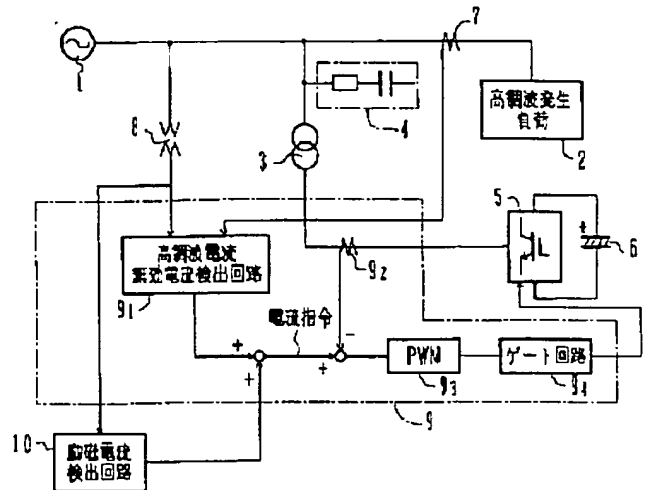


# Patent Abstracts of Japan

TITLE : ACTIVE FILTER



**SOLUTION:** When harmonics and reactive current generated by a load 2 are detected by a detection circuit 9<sub>1</sub> of a control circuit 9, the gate signal of a PWM waveform is generated by a PWM circuit 9<sub>3</sub> with it as a compensation current instruction, the gate control of an inverter main circuit 5 is made by a gate circuit 9<sub>4</sub>, and a compensation current from the inverter main circuit 4 is supplied to a power line via the transformer 3, an excitation current detection circuit 10 detects the primary voltage waveform of the transformer 3 as the excitation current waveform of the transformer 3 and adds it to the compensation current, thus compensating for the current corresponding to excitation of the transformer 3.

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